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Variable capacitance capacitor array.

A capacitor array provides a variable capacitance, having FET's as respective switching elements (10, 11, 12) for switching each capacitor (20, 21, 22) in the array on and off, and means (8) for controlling switching of the switching elements to switch selected ones of the capacitors on, characterised in that the control means are arranged to co-operate with voltage generation means (7) so as to supply to a control electrode of each switching element for switching on a selected said capacitor a voltage which causes the impedance provided by said switching element to change between multiple levels.

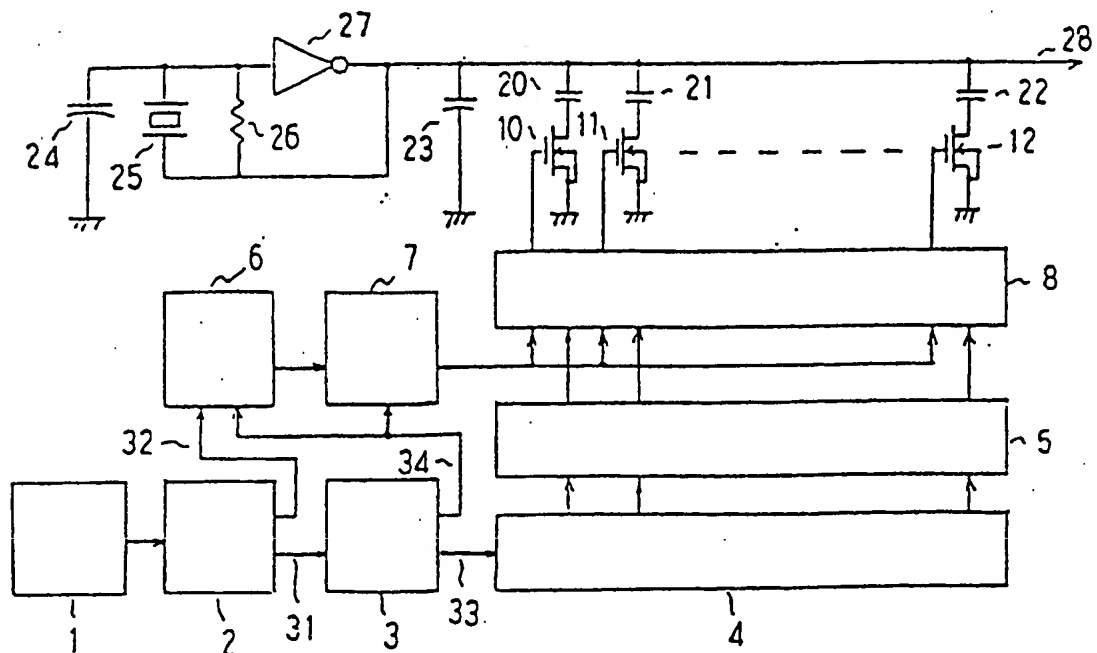


FIG. 1



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EUROPEAN SEARCH REPORT

Application Number

EP 90 31 3141

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. CL.5)
A	PATENT ABSTRACTS OF JAPAN vol. 11, no. 277 (E-538)(2724) September 8, 1987 & JP-A-62 76 801 (NEC CORP) April 4, 1987 * the whole document *	1,5,7,8	H03L1/02
A	PATENT ABSTRACTS OF JAPAN vol. 11, no. 292 (E-543)(2739) September 19, 1987 & JP-A-62 91 006 (MATSUSHITA KOGYO CO LTD) April 25, 1987 * the whole document *	1,5,7,8	
A	PATENT ABSTRACTS OF JAPAN vol. 12, no. 330 (E-655)(3177) September 7, 1988 & JP-A-63 93 205 (MATSUSHITA KOGYO CO LTD) April 4, 1988 * the whole document *	1,5,7,8	
A	EP-A-133 388 (THOMSON-CSF) * page 2, line 28 - page 4, line 21; figure 1 *	1,5,7,8	
A	PROCEEDINGS OF THE IEEE 1989 CUSTOM INTEGRATED CIRCUITS CONFERENCE, MAY 15-18, PAGES 1611-1615, NEW YORK, U.S.A. ROESGEN AND WARREN: "AN ANALOG FRONT END CHIP FOR V.32 MODEMS".		
			TECHNICAL FIELDS SEARCHED (Int. CL.5)
			H03L H03H H03B
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 14 AUGUST 1991	Examiner PEETERS M.M.G.
CATEGORY OF CITED DOCUMENTS		1 : theory or principle underlying the invention F : earlier patent document, but published on, or after the filing date D : document cited in the application I : document cited for other reasons X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document & : member of the same patent family, corresponding document	